ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	SYSTEM AND METHOD FOR ARBITRATION BETWEEN SHARED PERIPHERAL CORE DEVICES IN SYSTEM ON CHIP ARCHITECTURES

Application Number :

Confirmation Number:

First Named Applicant: Serafino Bueti

Attorney Docket Number: BUR920040148US1

Art Unit:

Examiner:

Search string: (6262594 or 6496880 or 6587905 or 6587932 or 6636949 or 20020091826 or 20030018738 or 20030051103 or 20030202530).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6262594	2001-07-17	Cheung et al.		326	38
	2	6496880	2002-12-17	Ma et al.		710	38
	3	6587905	2003-06-01	Correale, Jr. et al.		310	107
	4	6587932	2003-07-01	Tournier		711	151
	5	6636949	2003-10-21	Barroso et al.		711	141

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20020091826	2002-07-11	Comeau et al.		709	226
	2	20030018738	2003-01-23	Boylan et al.		709	213
	3	20030051103	2003-03-13	Larnder et al.		711	147
	4	20030202530	2003-10-30	Jenkins et al.		370	447

Signature

Examiner Name	Date